

Amendments to the Claims:

This listing of claims replaces all prior versions and listings of claims in the application:

Listing of Claims:

1. (Original) A method comprising:
monitoring a bit in a coprocessor included in a packet engine that represents an operation associated with a packet processor that includes the packet engine; and
providing the packet engine the status of the bit.
2. (Original) The method of claim 1 wherein monitoring the bit includes maintaining an indicator representing the status of the bit.
3. (Original) The method of claim 1 wherein monitoring the bit includes maintaining an index identifying the bit.
4. (Original) The method of claim 1 wherein monitoring the bit includes maintaining an indicator representing completion of monitoring of the bit.
5. (Original) The method of claim 1 wherein monitoring the bit includes applying a logical mask to the bit.
6. (Original) The method of claim 1 wherein the bit represents servicing status of a digital subscriber line.
7. (Original) The method of claim 1 wherein the bit is a portion of a word.

8. (Currently amended) A computer program product, residing on a computer readable medium, comprising instructions for causing tangibly embodied in an information carrier,
~~the computer program product being operable to cause a machine to:~~
monitor a bit in a coprocessor included in a packet engine that represents an operation associated with a packet processor that includes the packet engine; and
provide the packet engine the status of the bit.
9. (Original) The computer program product of claim 8 wherein monitoring the bit includes maintaining an indicator representing the status of the bit.
10. (Original) The computer program product of claim 8 wherein monitoring the bit includes maintaining an index identifying the bit.
11. (Original) The computer program product of claim 8 monitoring the bit includes maintaining an indicator representing completion of monitoring of the bit.
12. (Original) The computer program product of claim 8 wherein monitoring the bit includes applying a logical mask to the bit.
13. (Original) The computer program product of claim 8 wherein the bit represents servicing status of a digital subscriber line.
14. (Original) The computer program product of claim 8 wherein the bit is a portion of a word.
15. (Currently amended) A line monitor comprises:
a computing device executing:

_____ a process to monitor a bit in a coprocessor included in a packet engine that represents an operation associated with a packet processor that includes the packet engine; and

_____ a process to provide the packet engine the status of the bit.

16. (Original) The line monitor of claim 15 wherein monitoring the bit includes maintaining an indicator representing the status of the bit.
17. (Original) The line monitor of claim 15 wherein monitoring the bit includes maintaining an index identifying the bit.
18. (Original) The line monitor of claim 15 wherein monitoring the bit includes maintaining an indicator representing completion of monitoring of the bit.
19. (Original) The line monitor of claim 15 wherein monitoring the bit includes applying a logical mask to the bit.
20. (Original) The line monitor of claim 15 wherein the bit represents servicing status of a digital subscriber line.
21. (Original) The line monitor of claim 15 wherein the bit is a portion of a word.
22. (Original) A system comprising:
 - a coprocessor included in a packet engine that is capable of,
 - monitoring a bit representing an operation associated with a packet processor that includes the packet engine; and
 - providing the packet engine the status of the bit.

23. (Original) The system of claim 22 wherein monitoring the bit includes maintaining an indicator representing the status of the bit.
24. (Original) The system of claim 22 wherein monitoring the bit includes maintaining an index identifying the bit.
25. (Original) A packet forwarding device comprising:
 - an input port for receiving packets;
 - an output for delivering the received packets; and
 - a coprocessor included in a packet engine that is capable of,
 - monitoring a bit representing an operation associated with a packet processor that includes the packet engine, and
 - providing the packet engine the status of the bit.
26. (Original) The packet forwarding device of claim 25 wherein monitoring the bit includes maintaining an indicator representing the status of the bit.
27. (Original) The packet forwarding device of claim 25 wherein monitoring the bit includes maintaining an index identifying the bit.
28. (Original) A method comprising:
 - monitoring a bit in a monitoring coprocessor included in a network processing engine that represents the servicing availability of a digital subscriber line associated with a network processor that includes the network processing engine; and
 - providing the network processing engine data representing the servicing availability of the digital subscriber line.

29. (Original) The method of claim 28 wherein monitoring the bit includes maintaining an indicator representing that the digital subscriber line is ready for servicing.
30. (Original) The method of claim 28 monitoring the bit includes maintaining an index variable that stores an integer identifying the digital subscriber line ready for servicing.